IN THE CLAIMS

Please add the following claims:

30. (New) An apparatus, comprising:

a first integrated circuit having multiple states of performance including a first

state of performance, a second state of performance higher than the first state of

performance, and a third state of performance higher than the second state of

performance, the first integrated circuit; and

a power manager to manage power consumption within the first integrated circuit,

the power manager to directly transition the first integrated circuit from the first state of

performance to the third state of performance based upon detecting a user event.

31. (New) The apparatus of claim 30, wherein the first state of performance comprises a

first voltage level and a first operating frequency.

32. (New) The apparatus of claim 30, wherein the third state of performance comprises a

second integrated circuit co-processing instructions with the first integrated circuit.

33. (New) The apparatus of claim 30, further comprising:

frequency regulation logic to change an operating frequency of the first integrated

circuit, the frequency regulation logic to receive a signal from the program.

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